



Attorney's Docket No.: 07470/020001

AF/2762
#18/B
M.C.
127-06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Marshall Isman Art Unit : 2762
Serial No.: 08/997,142 Examiner : W. Zhen
Filed : December 23, 1997
Title : METHOD FOR ANALYZING CAPACITY OF PARALLEL PROCESSING
SYSTEMS

BOX AF

Commissioner for Patents
Washington, D.C. 20231

RECEIVED
NOV 28 2000
Technology Center 2100

AMENDMENT AFTER FINAL

In response to the action mailed September 22, 2000, please
amend the application as follows:

Please amend claims 1 and 2 as follows:

1. (Twice amended) A method for analyzing the capacity of
an application executing on a parallel processing system and
expressed as a graph of vertices, comprising the steps of:

B1
(a) accessing an application for processing designated
data records on a parallel processing system, such application
being expressed as a graph of vertices;

(b) creating a description of the sizes of data
records throughout the graph;

(c) [(b)] creating a performance description of each
vertex in the graph;

CERTIFICATE OF MAILING BY FIRST CLASS MAIL
I hereby certify under 37 CFR §1.8(a) that this
correspondence is being deposited with the
United States Postal Service as first class mail
with sufficient postage on the date indicated
below and is addressed to the Commissioner for
Patents, Washington, D.C. 20231.

11/21/00
Date of Deposit

Signature *Kasey R. Cook*

Kasey R. Cook
Typed or Printed Name of Person Signing
Certificate

23

B